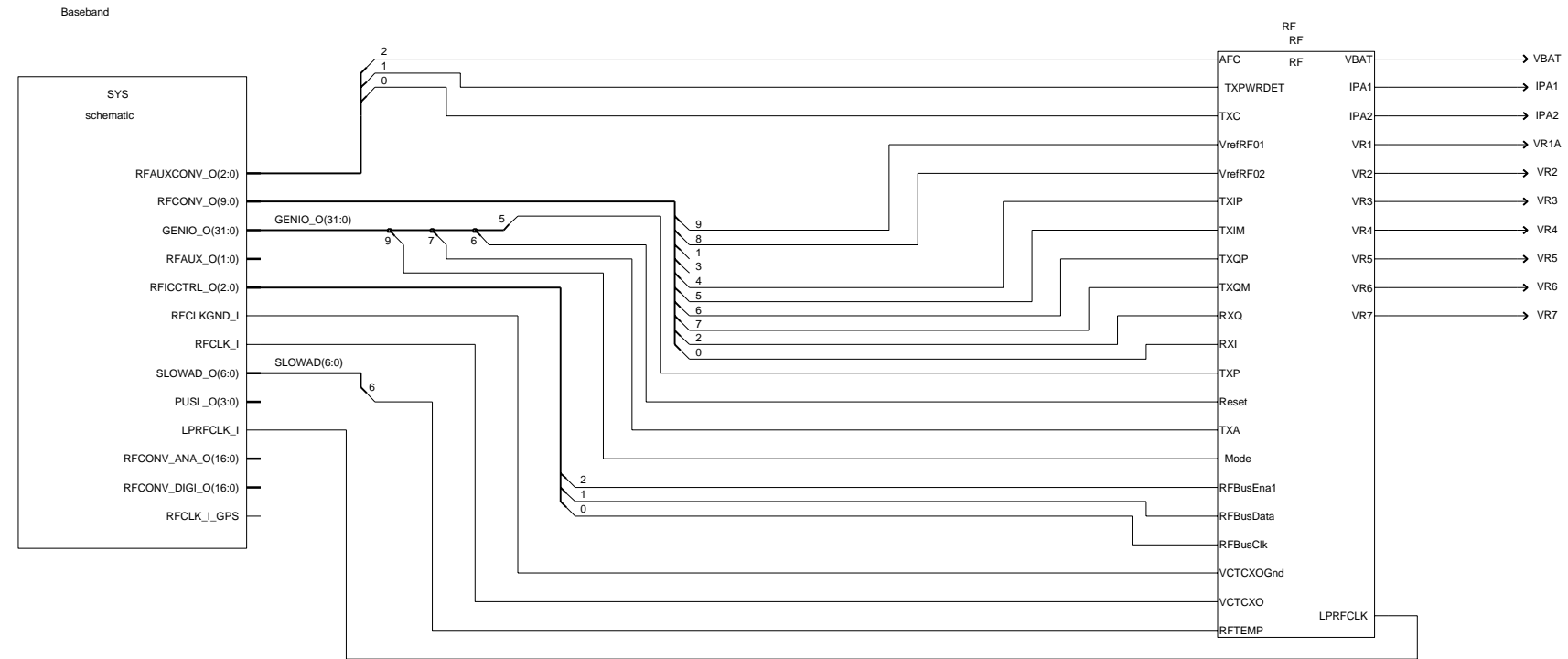
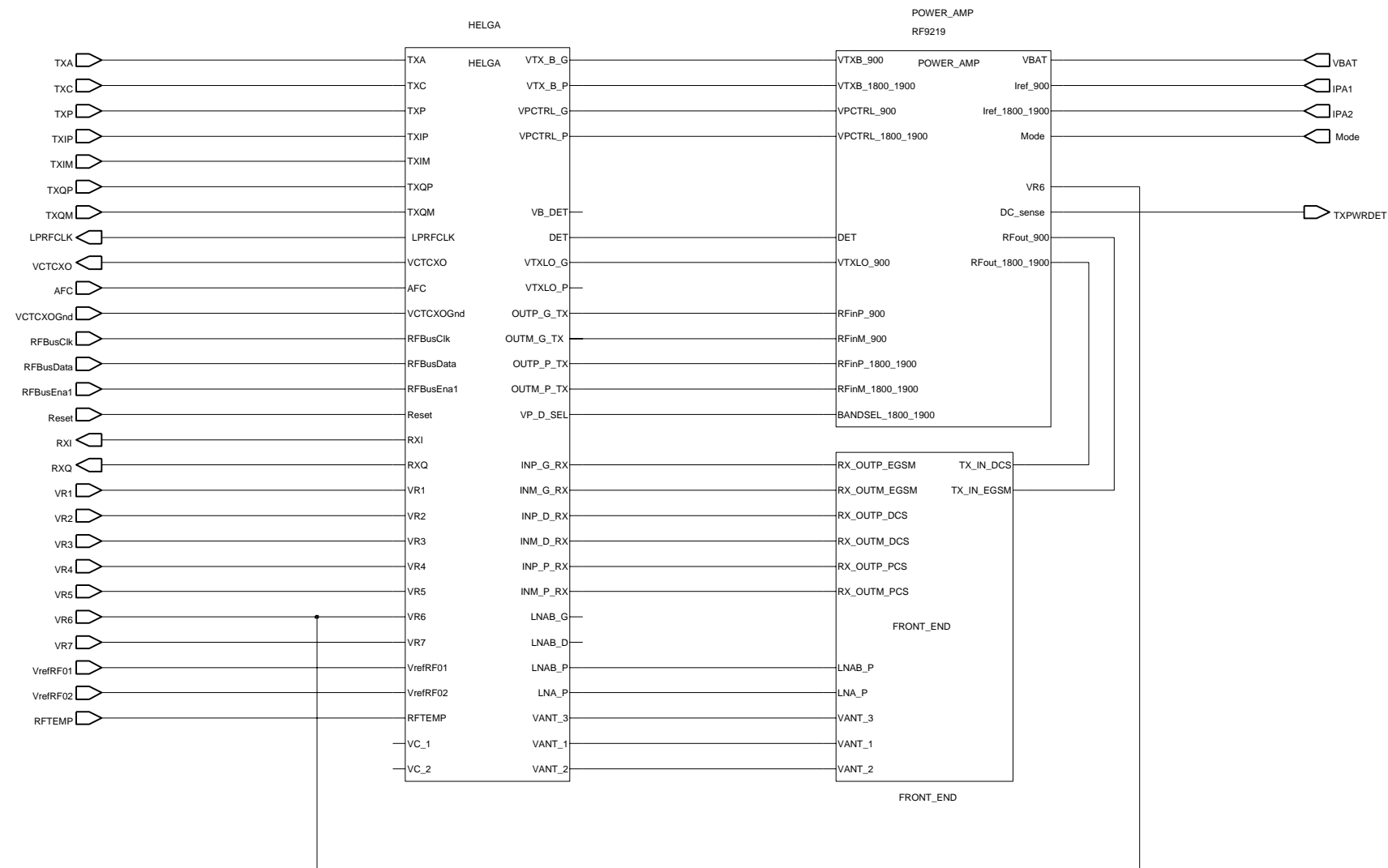


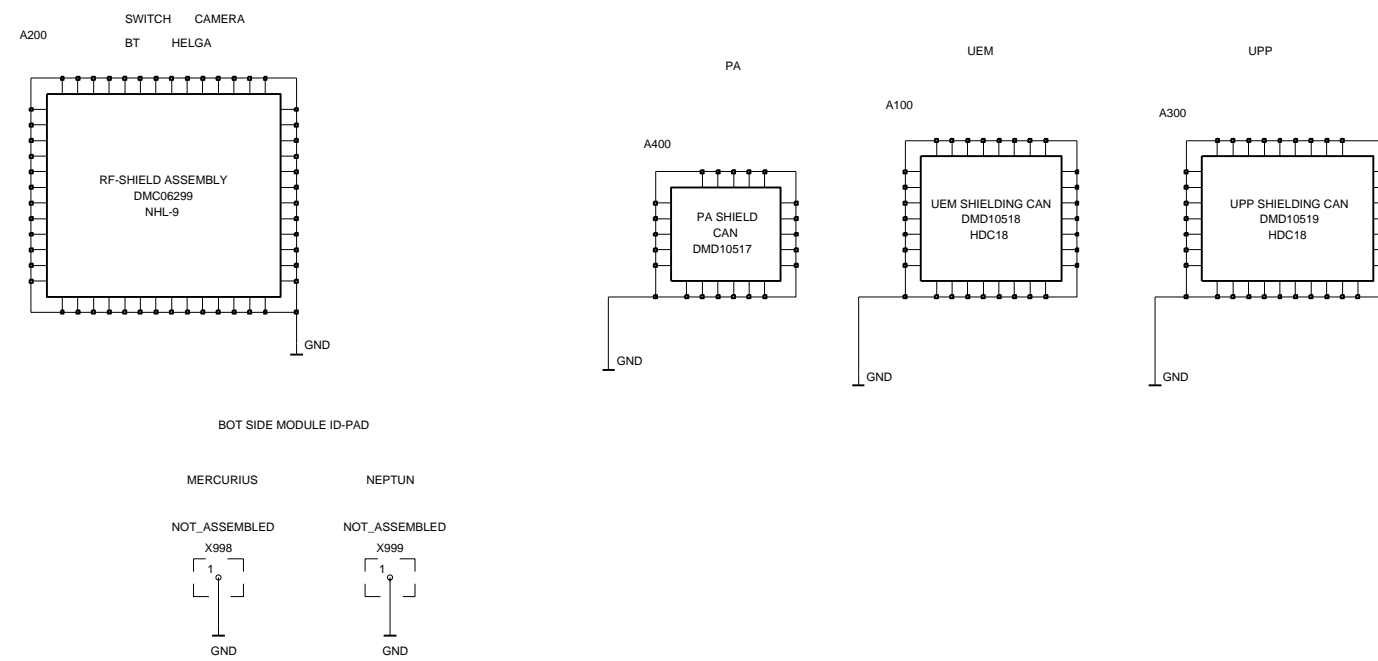
RF/BB Connections v. 0.0 ed.7



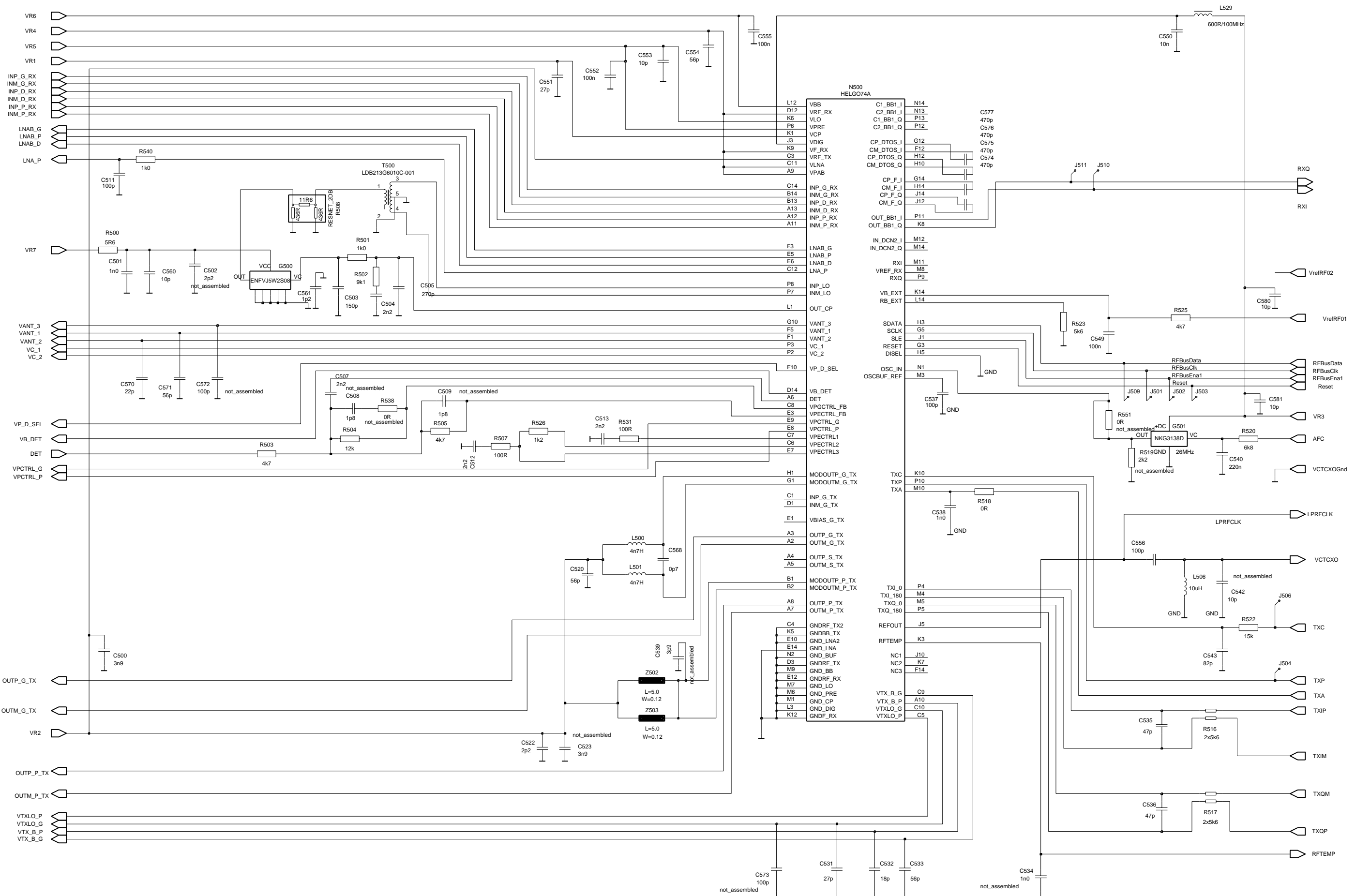
RF Block Diagram v.0.0 ed.13



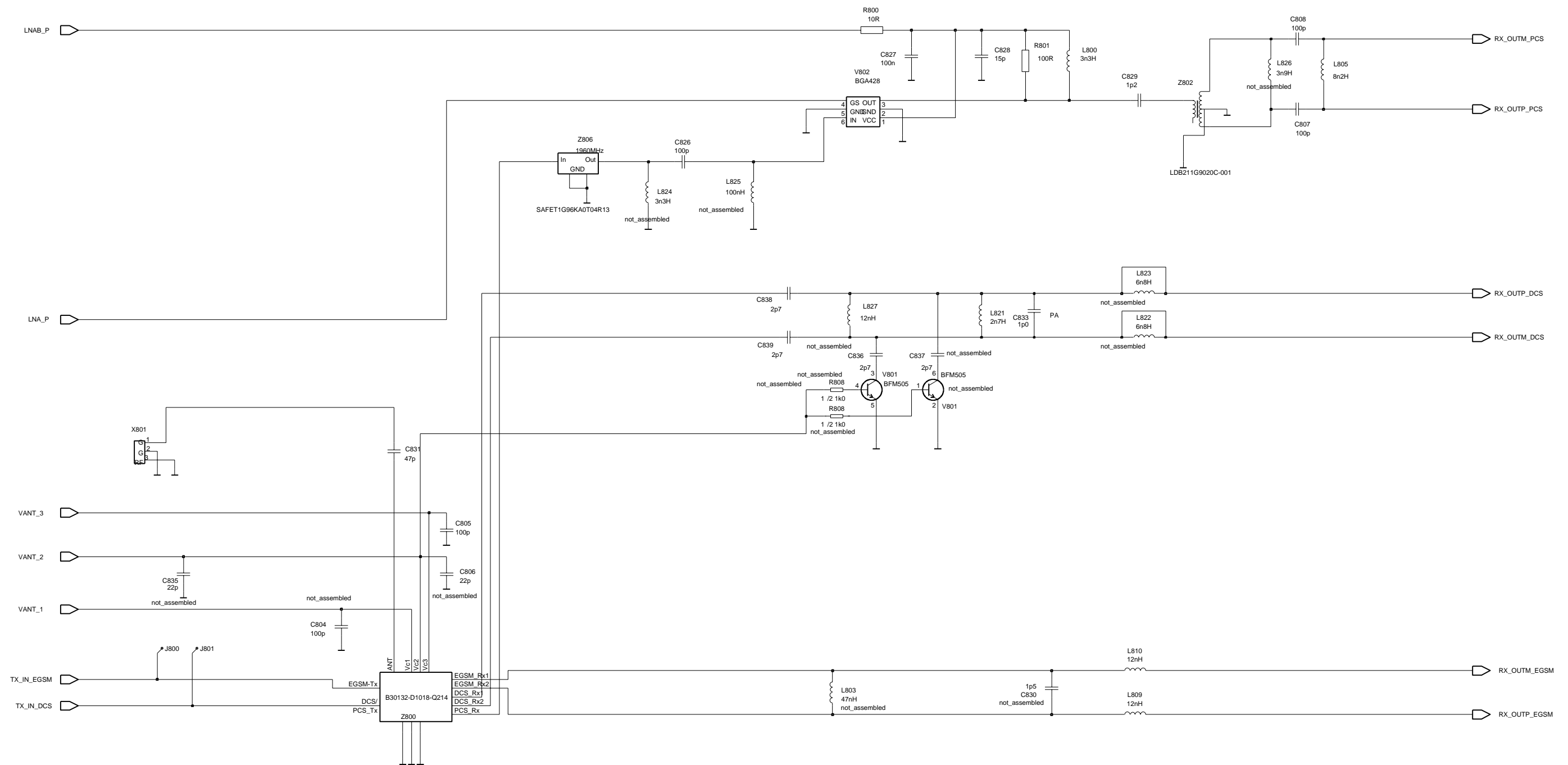
RF and BB Shields v. 0.0 ed. 10



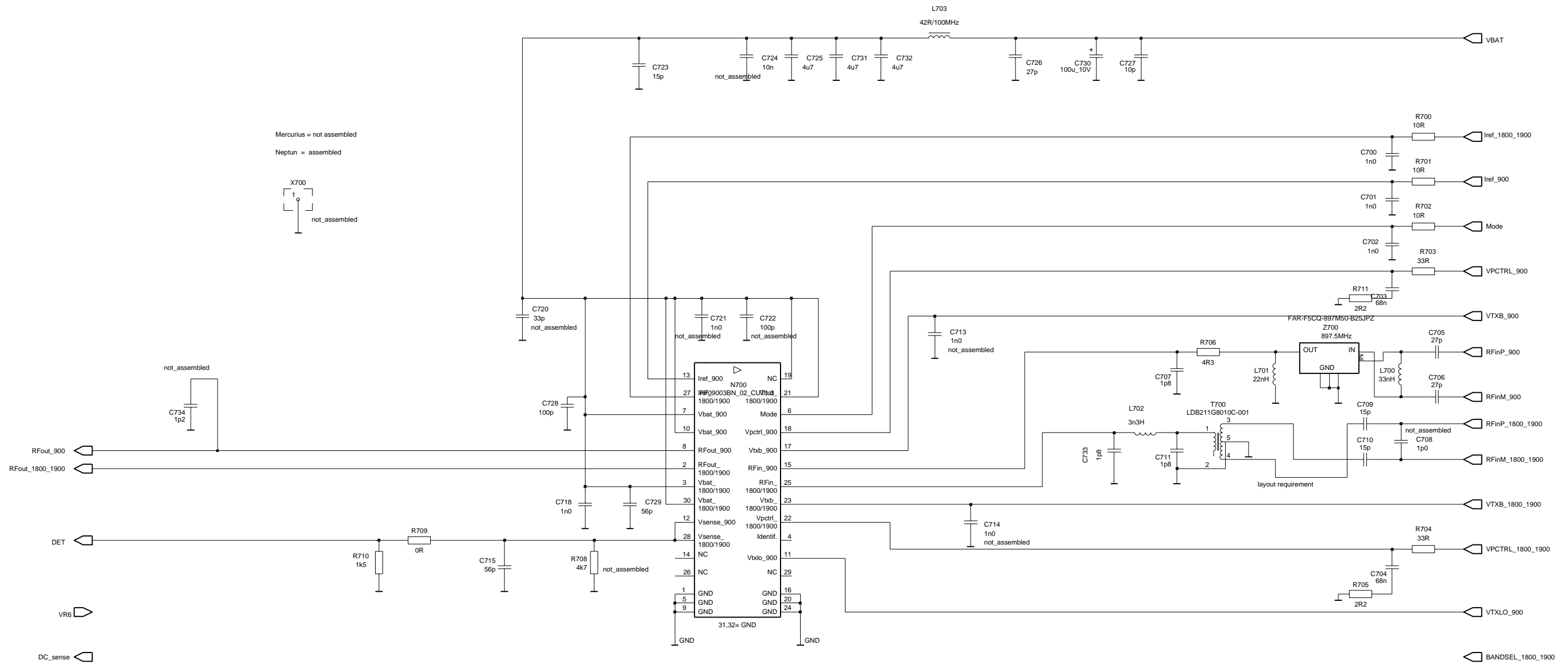
HELGA v. 0.0 ed. 48



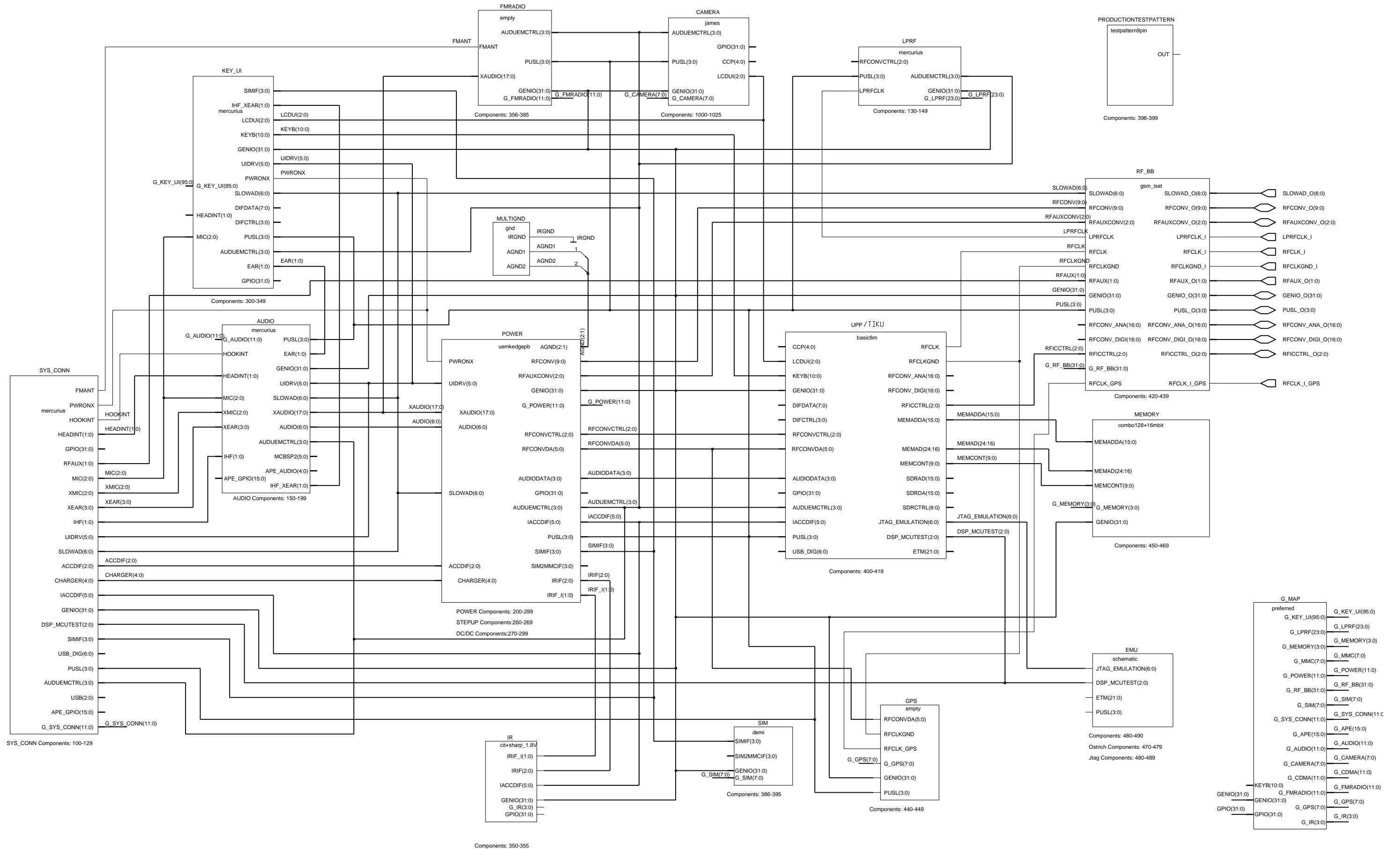
RX Front End and Antenna Switch v. 0.0 ed. 23



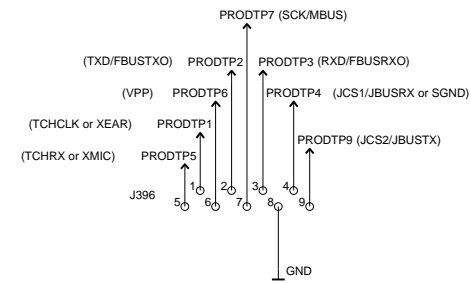
Power Amplifier and Power Detection 0.2 ed. 27



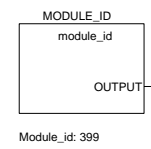
Baseband Block Diagram v. 5.0 ed. 33



9 pin Production Test Pattern v. 1.3 ed. 13



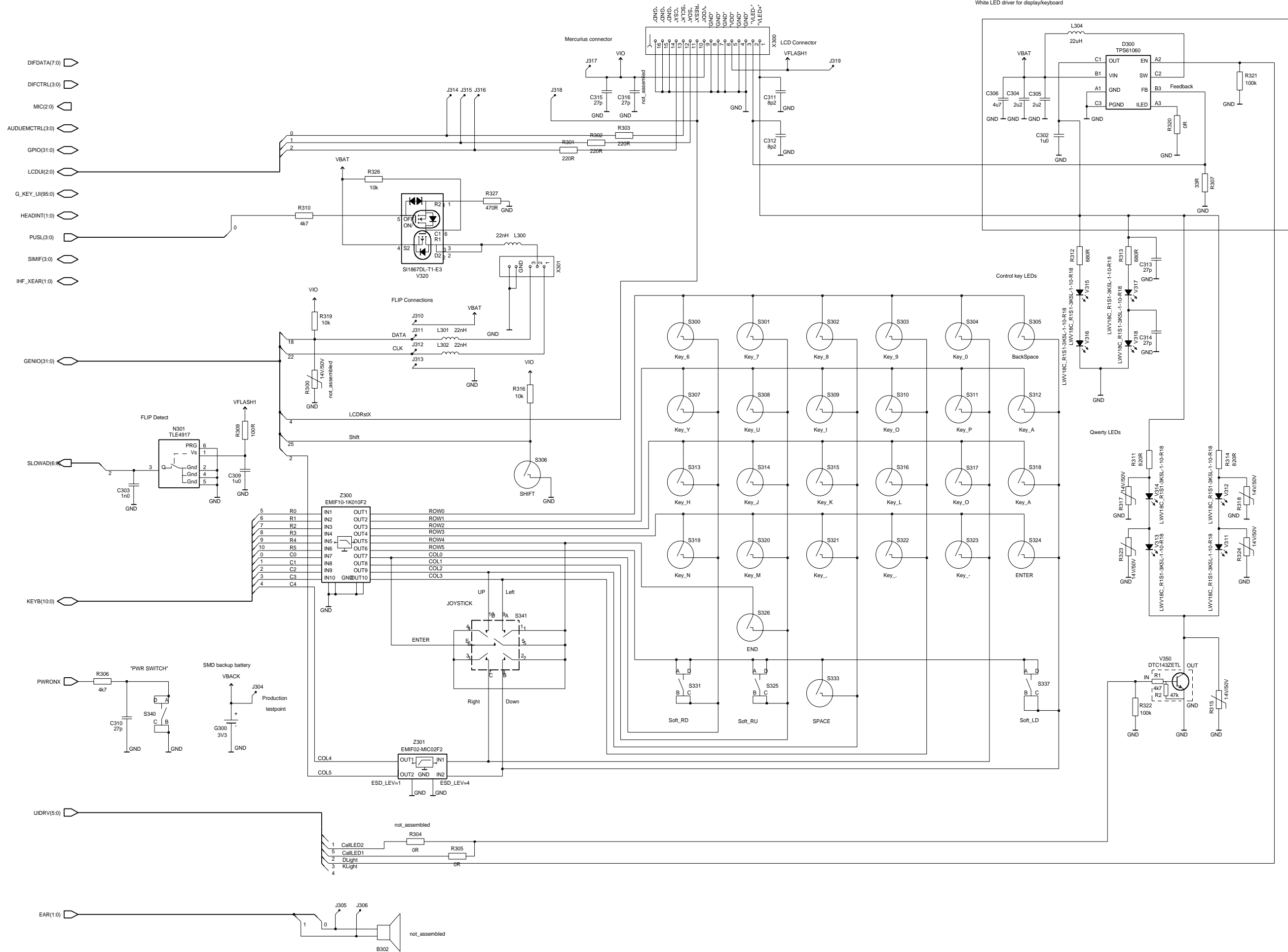
OUT



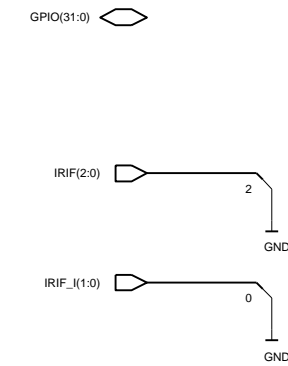
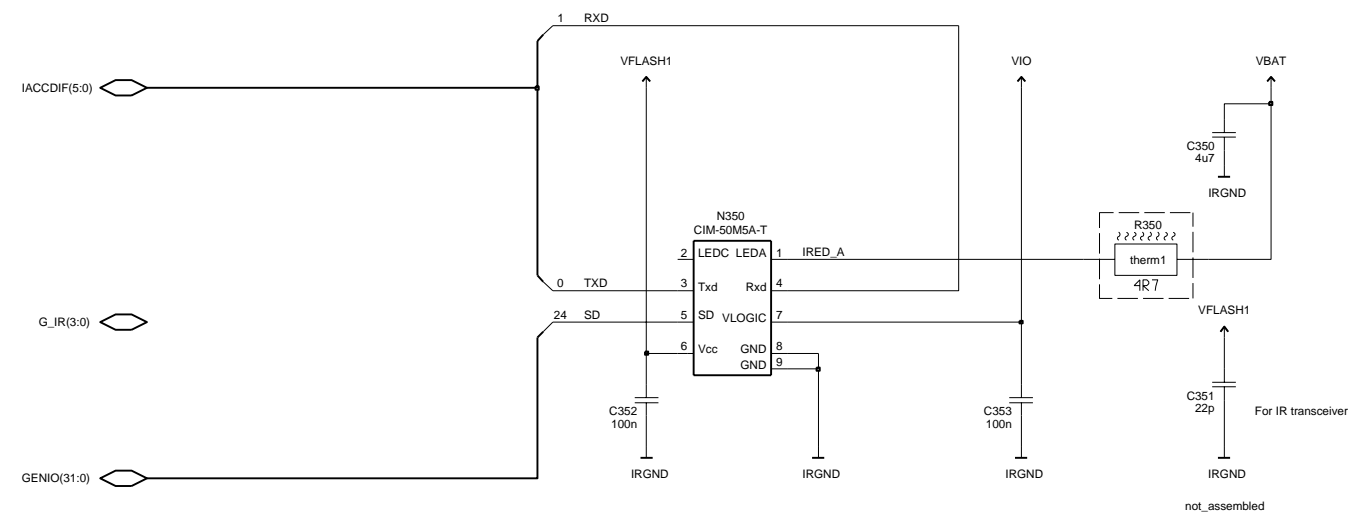
OUTPUT 



User Interface v. 0.0 ed.130



IR Module 1.8V v. 0.0 ed. 50



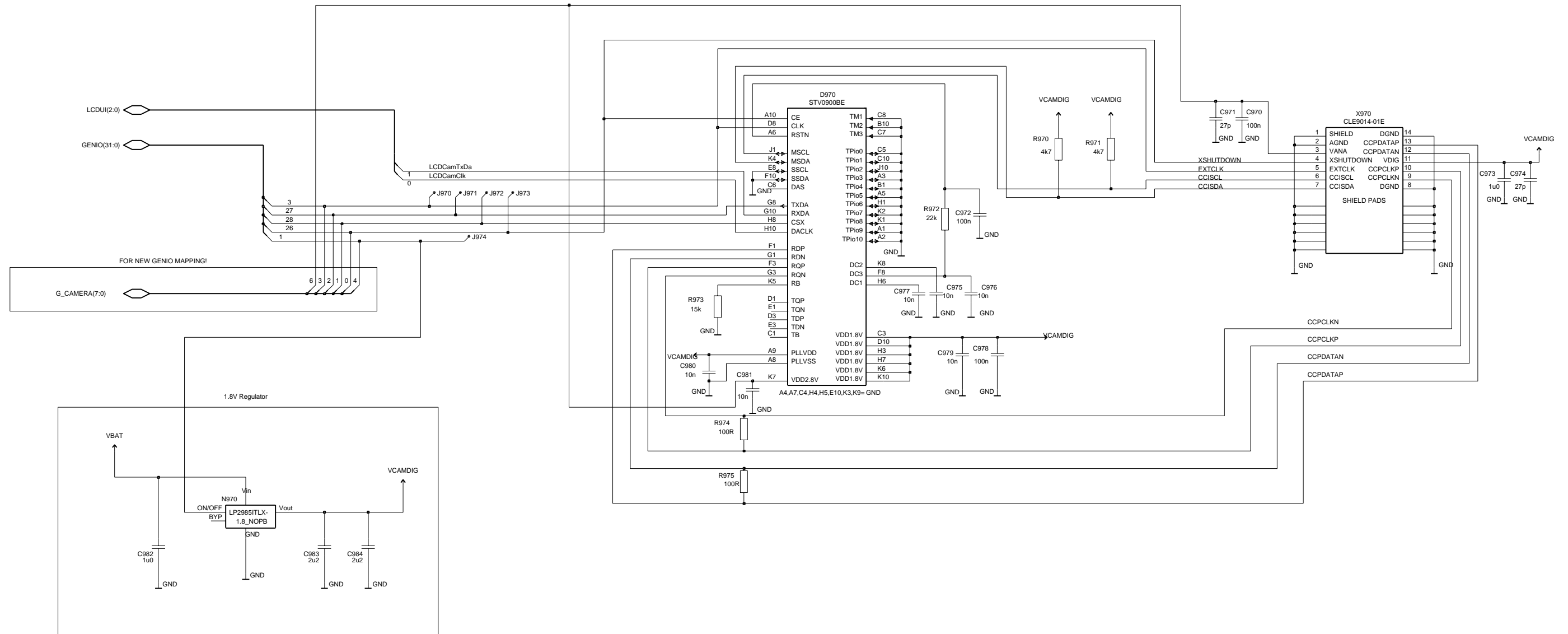
UEM IR level shifters
are ground, when 1.8V
IR is used!

Used references

- C 350 - 353
- N 350
- R 350

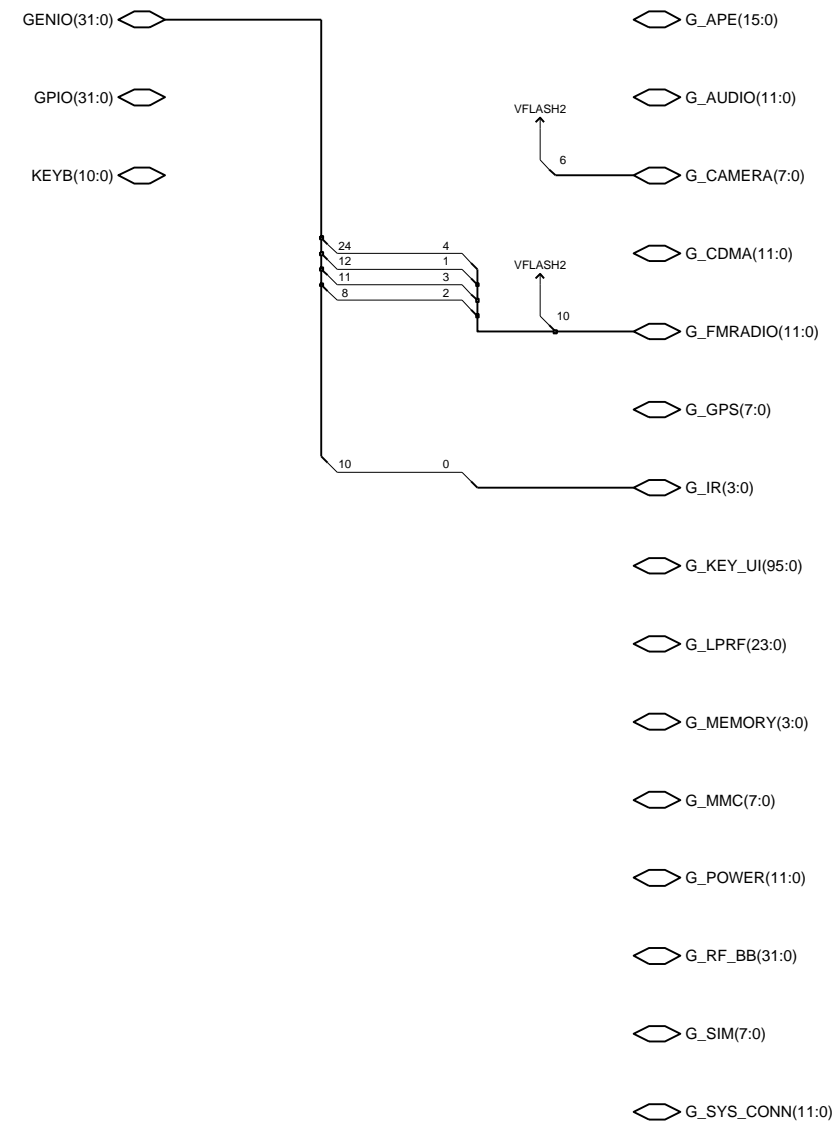
Camera v. 0 ed. 61

- GPI0(31:0)
- AUDUEMCTRL(3:0)
- CCP(4:0)
- PUSL(3:0)

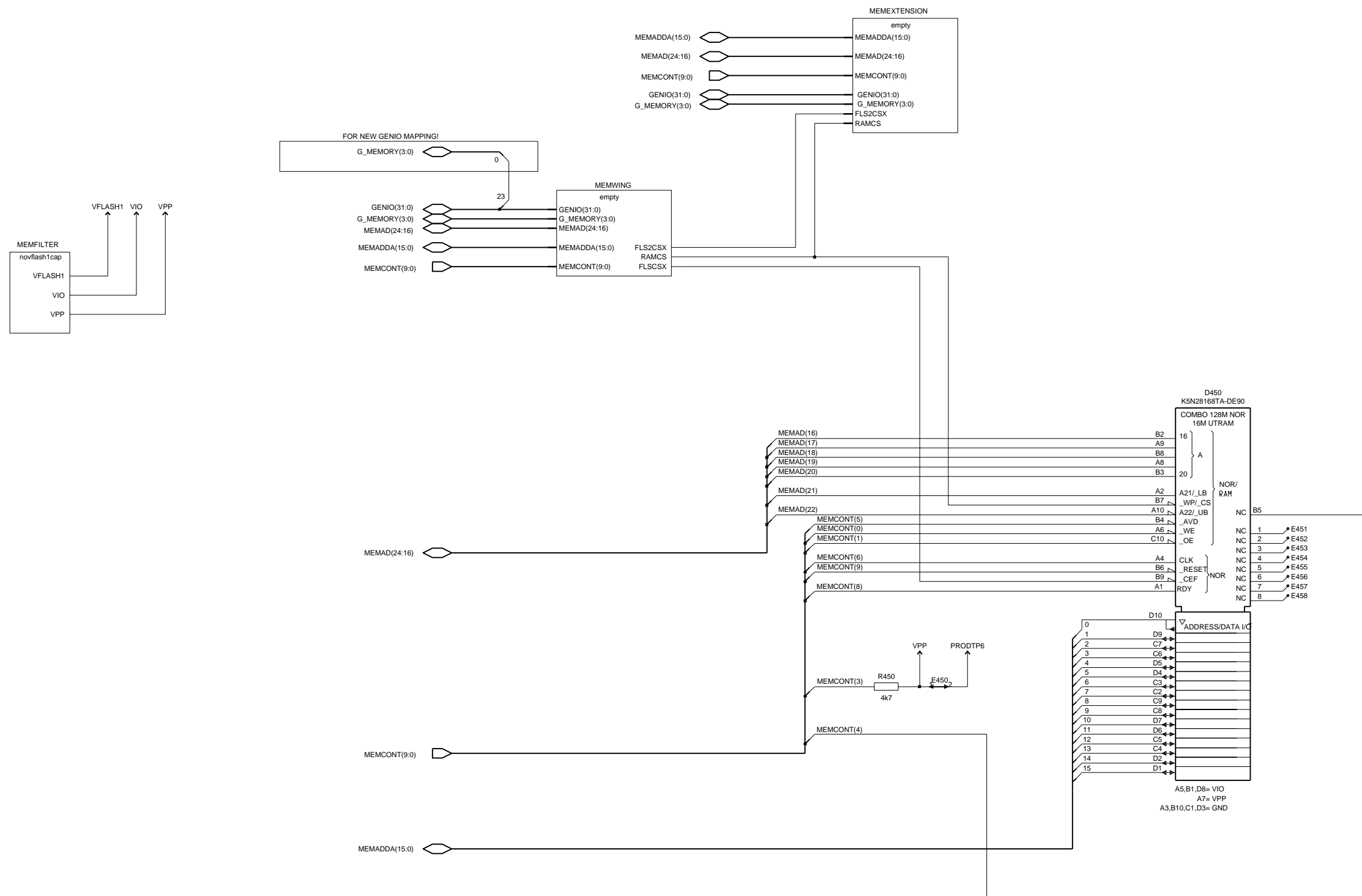


GENIO and GPIO Connection Block v. 0.0 ed. 8

That connections used only if TEA5767 or CIT+SHARP_1.8V schematics are selected



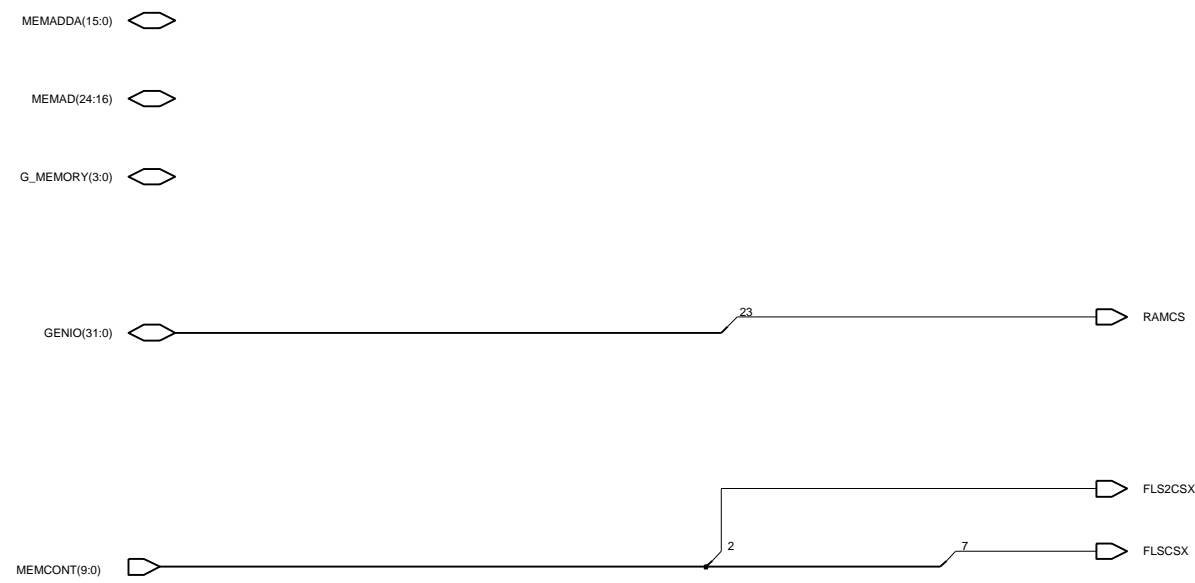
Combo Memory 128+16 Mbit v. 2.0 ed. 77



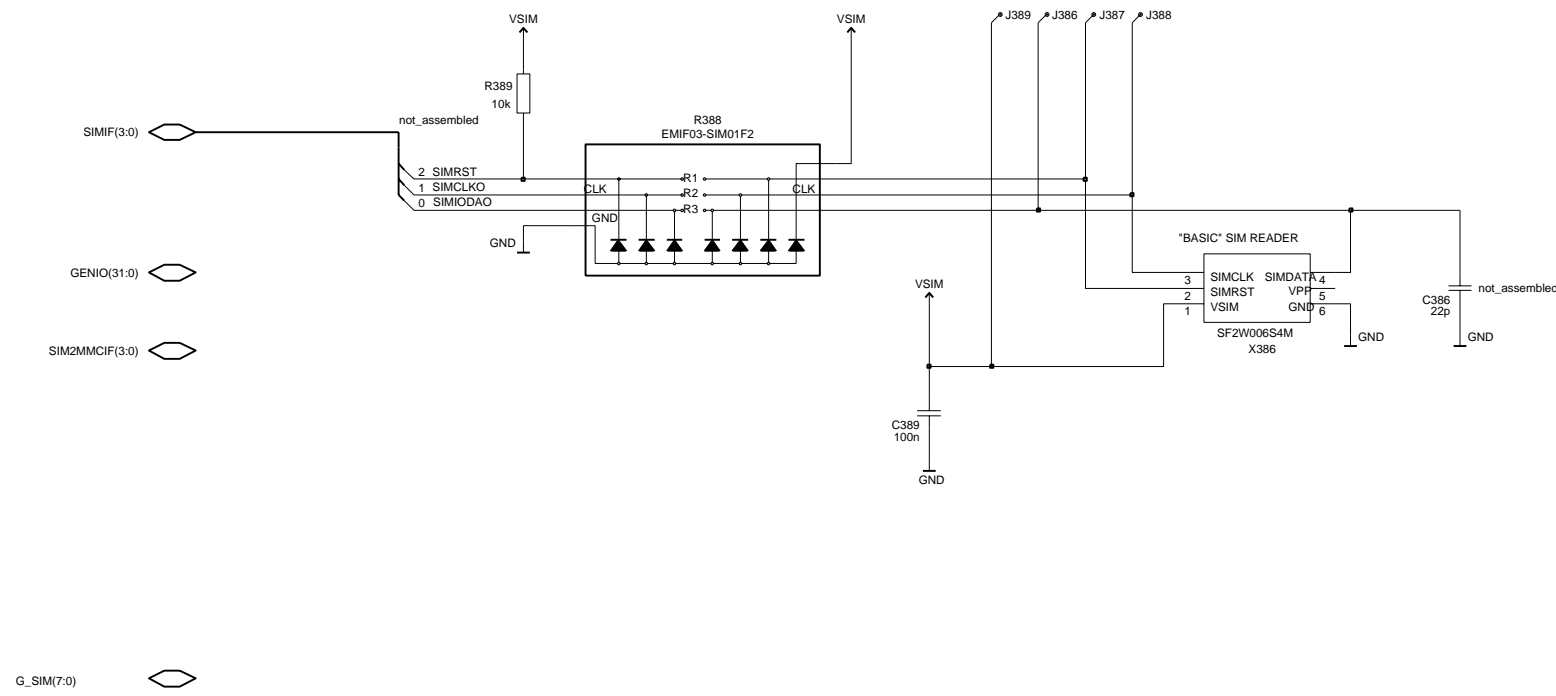
Discrete Capacitors for Memory without VFlash1 v. 1.3 ed. 11



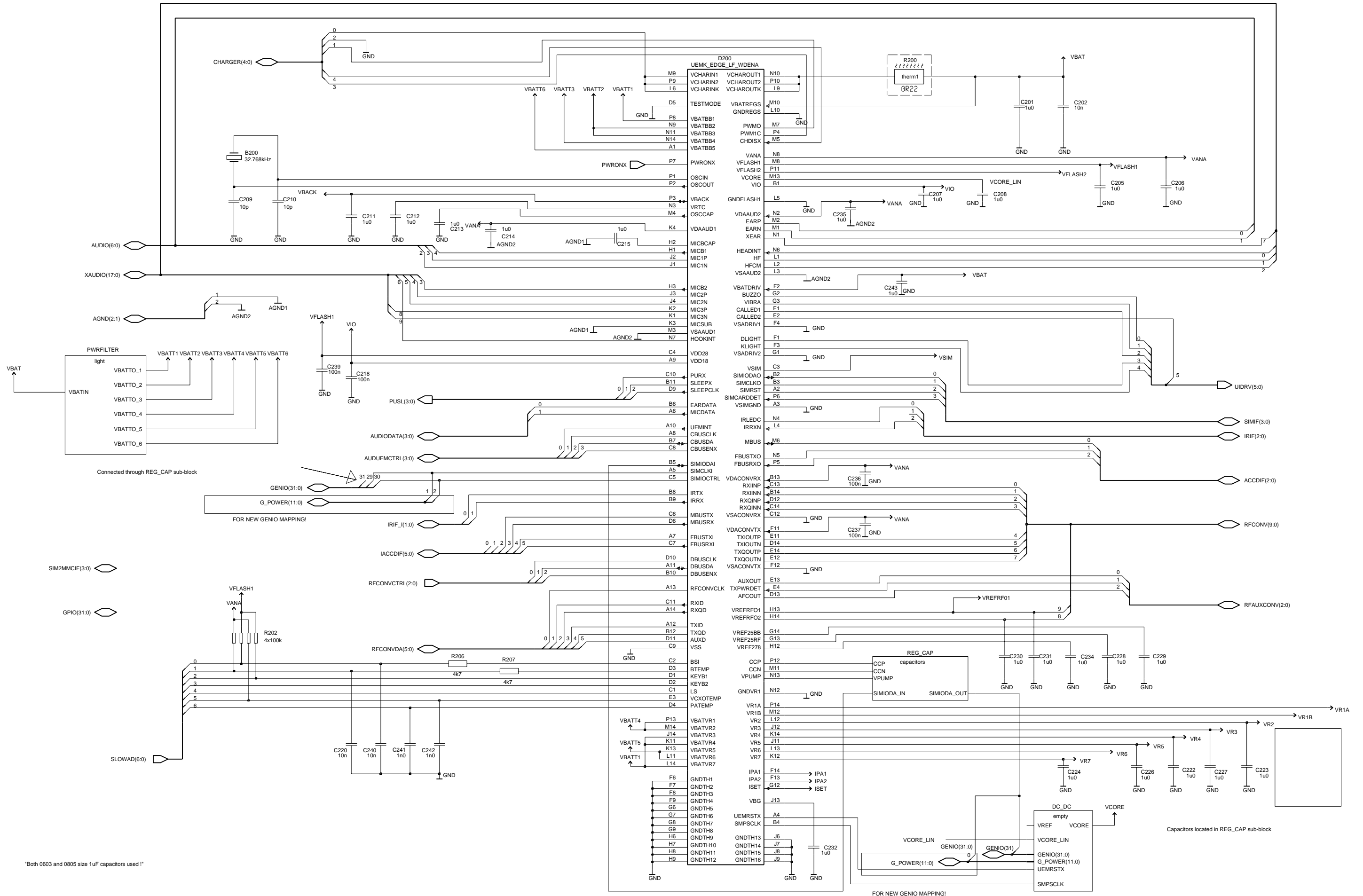
Empty Wing Sheet v. 0 ed. 6



SIM Reader v. 1.3 ed. 57



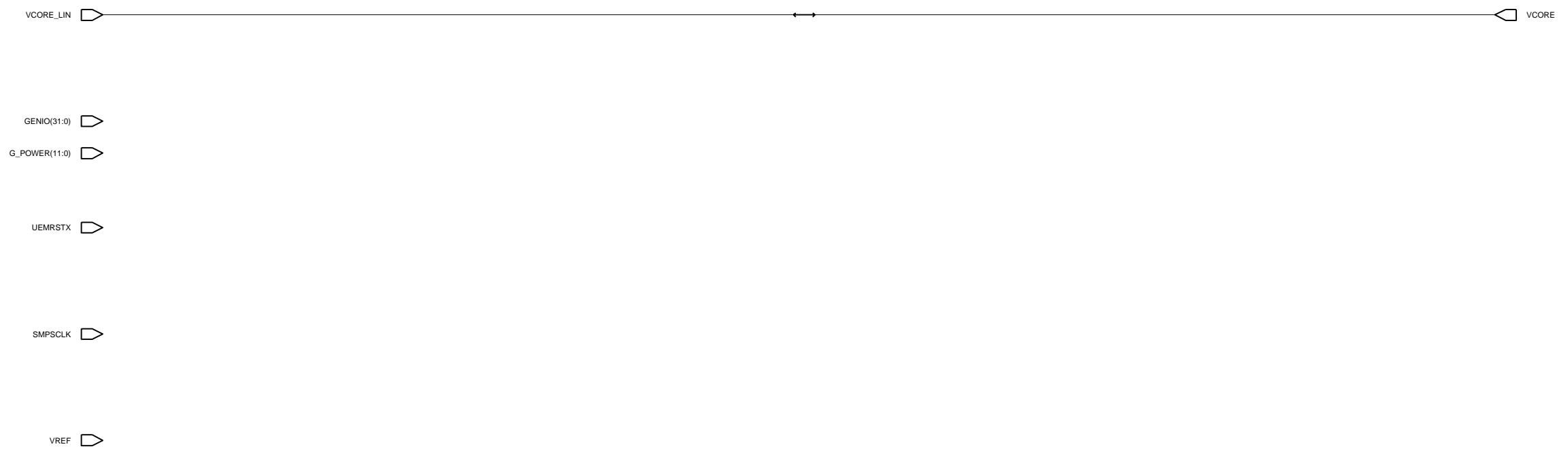
Discrete Power Management v. 1.3 ed. 125

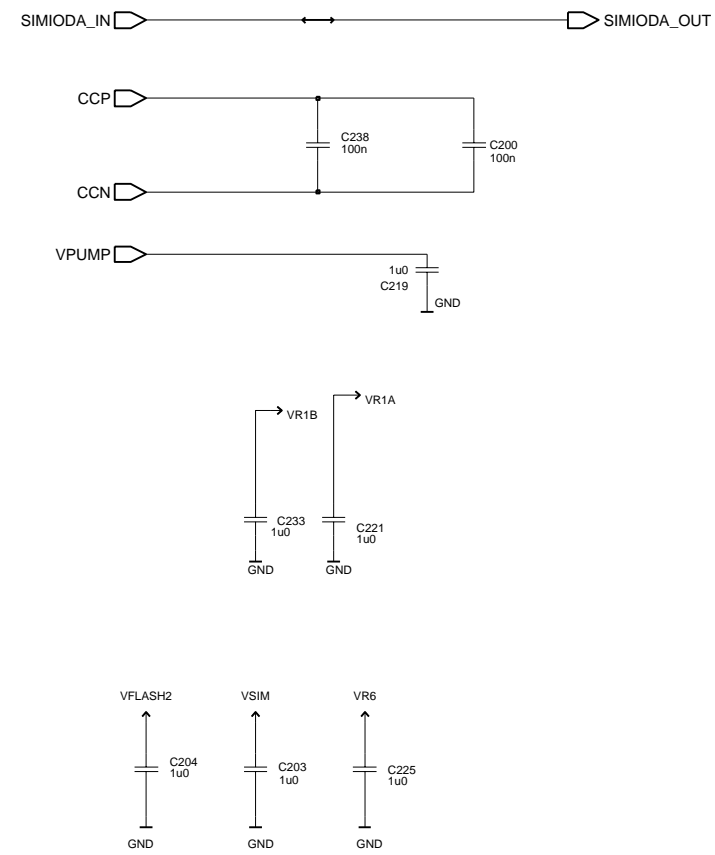


"Both 0603 and 0805 size 1uF capacitors used!"

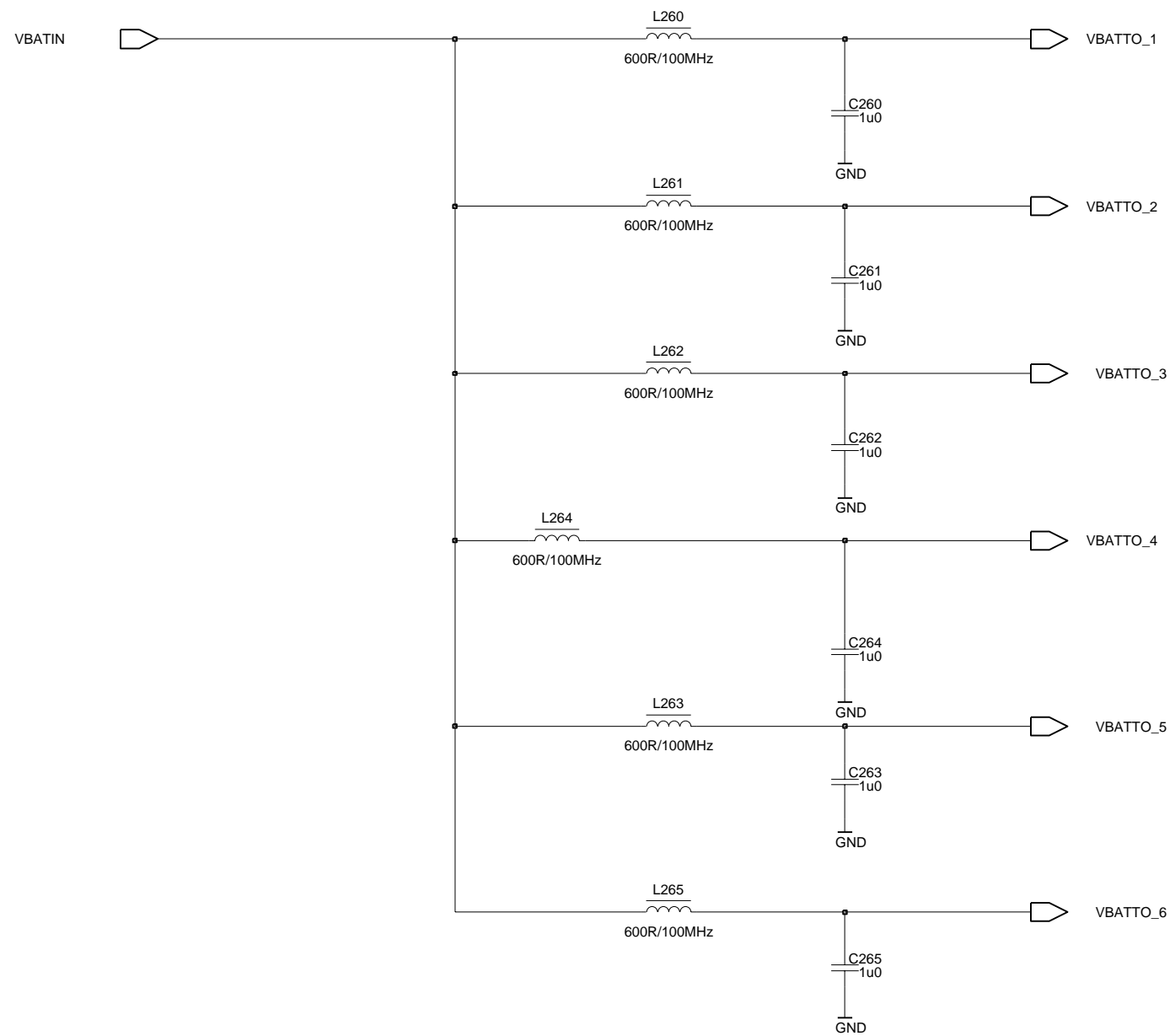
Used refs in REG_CAP: C238, C200, C219, C233, C221 and R205

DC/DC Converter v. 0.0 ed. 10

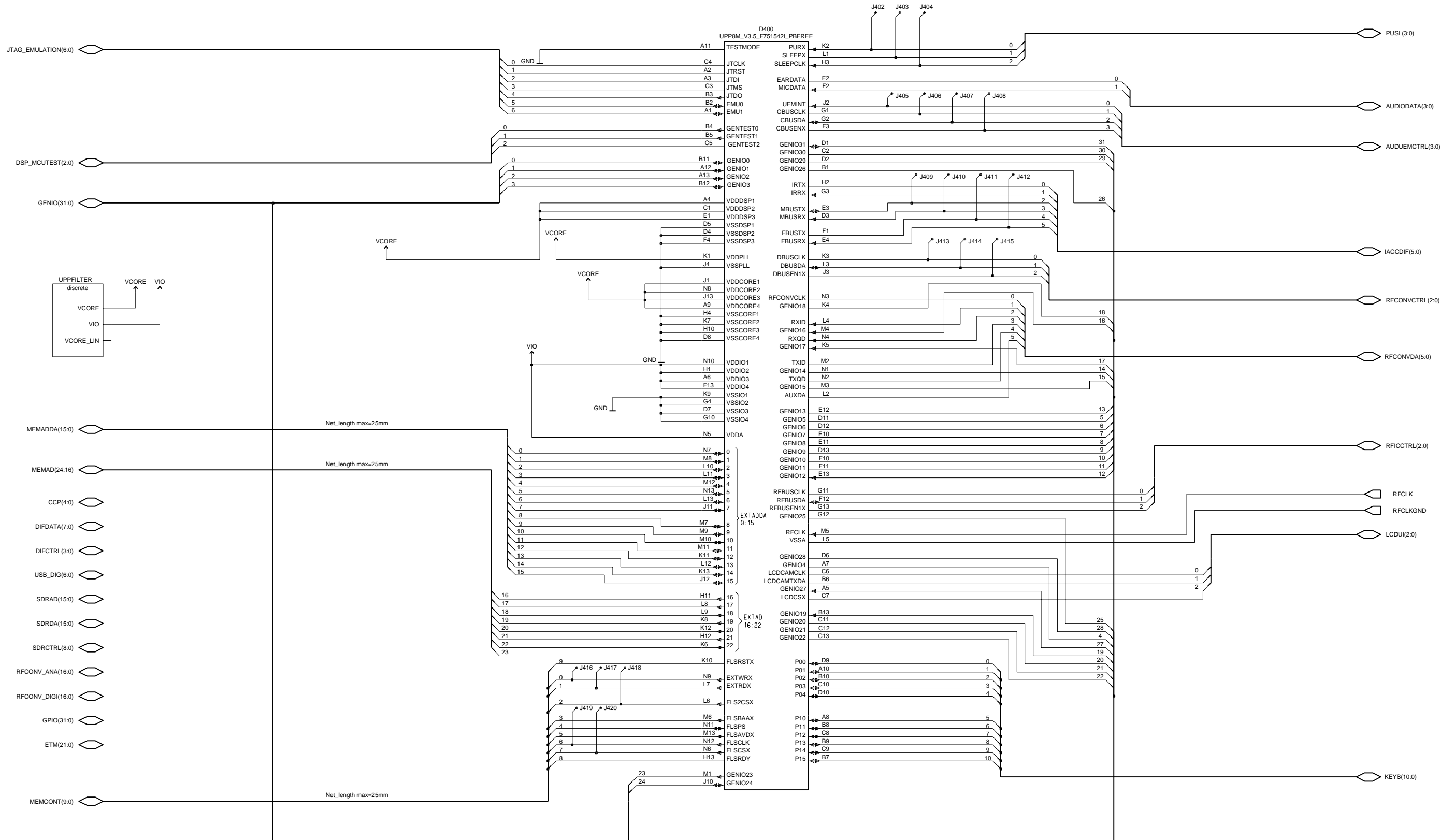




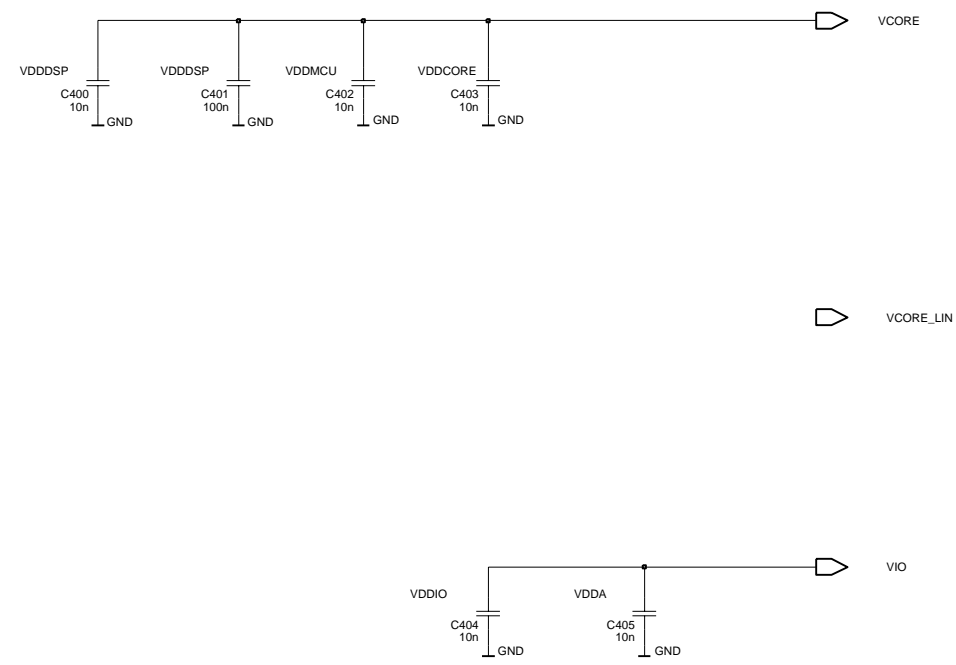
Light Filtering v. 2.0 ed. 28



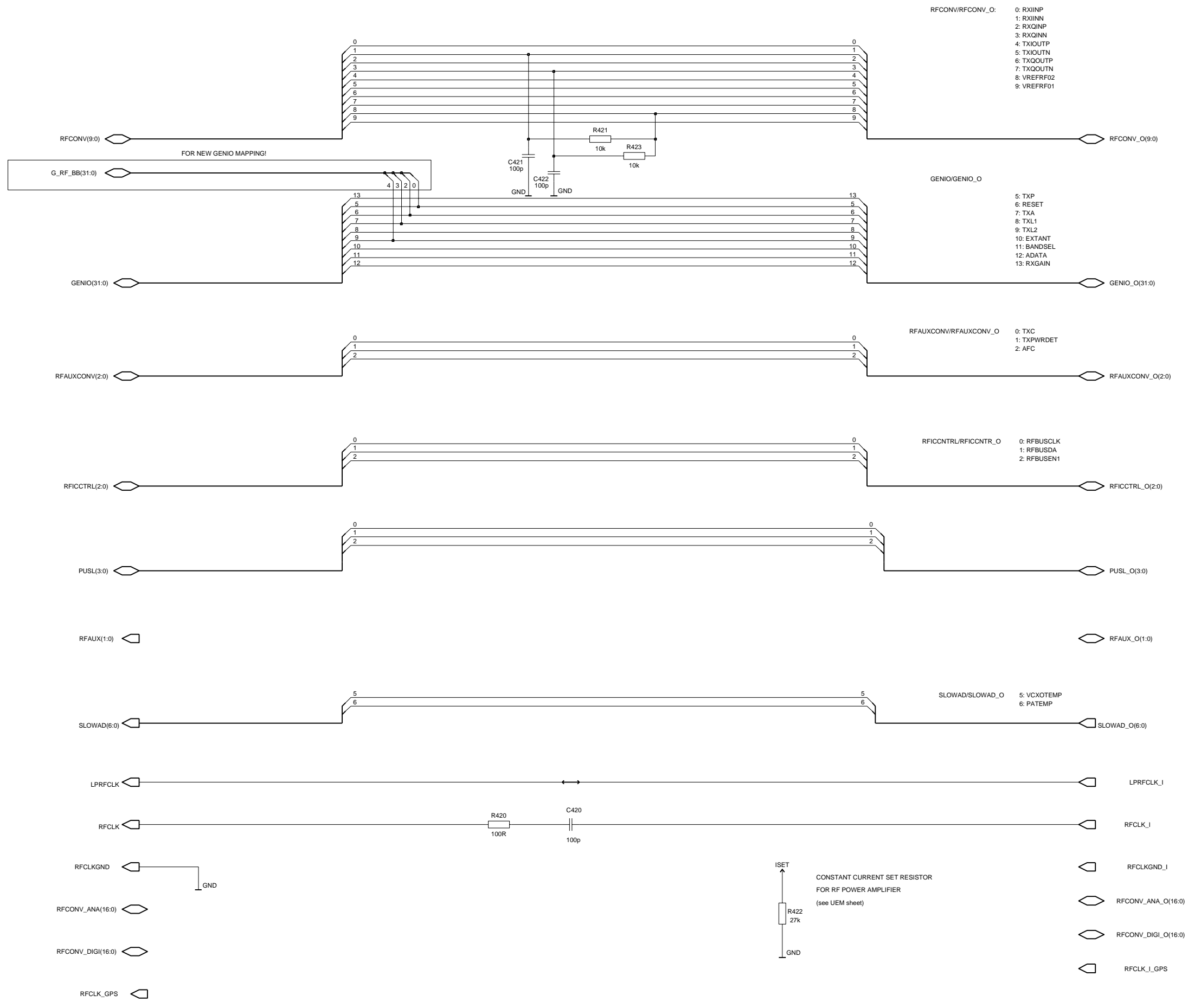
UPP 8M Implementation v. 2.0 ed. 105



Discrete Decoupling Capacitors for UPP v. 1.3 ed. 10

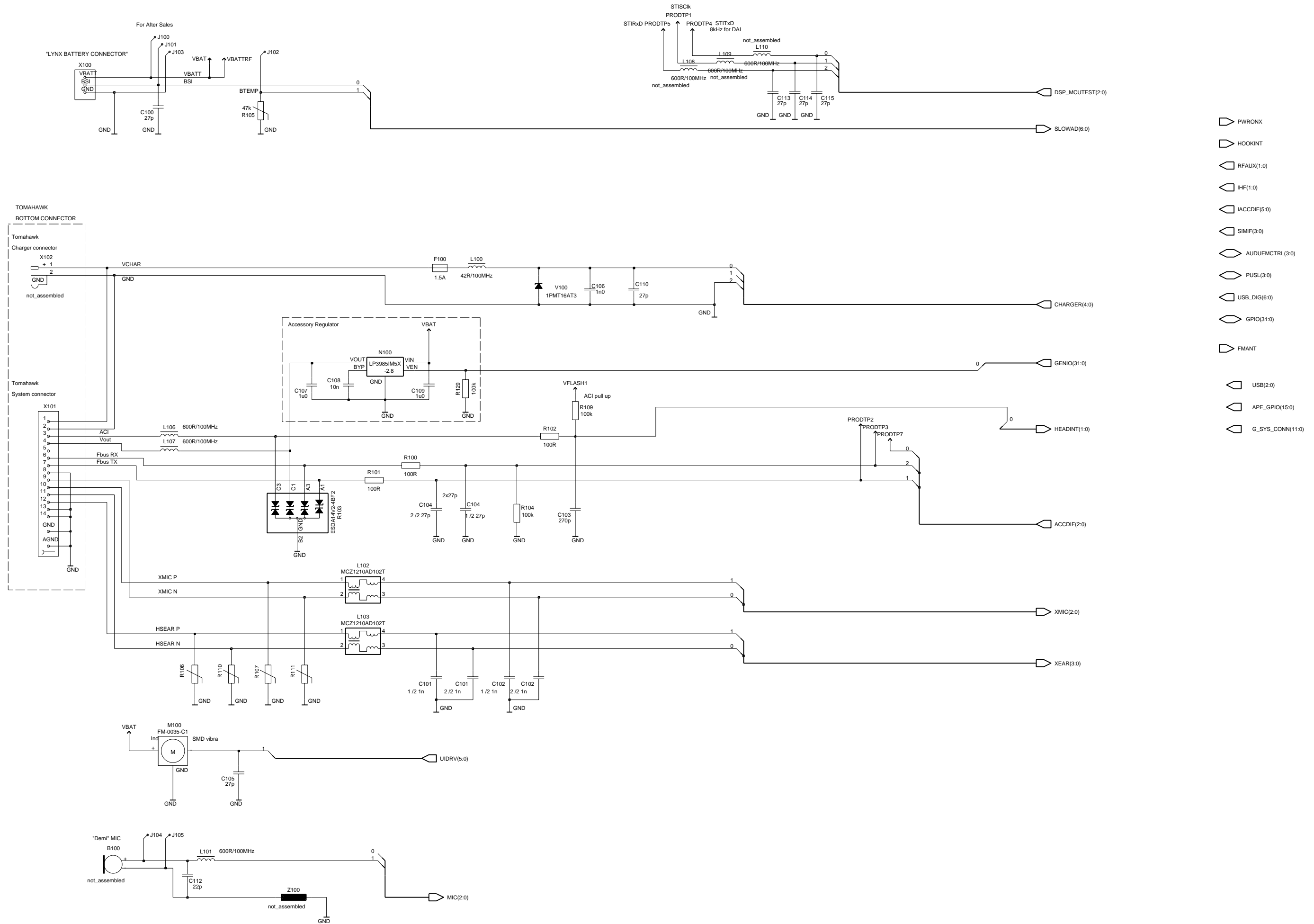


GSM RF - Baseband Interface v. 1.3 ed. 44



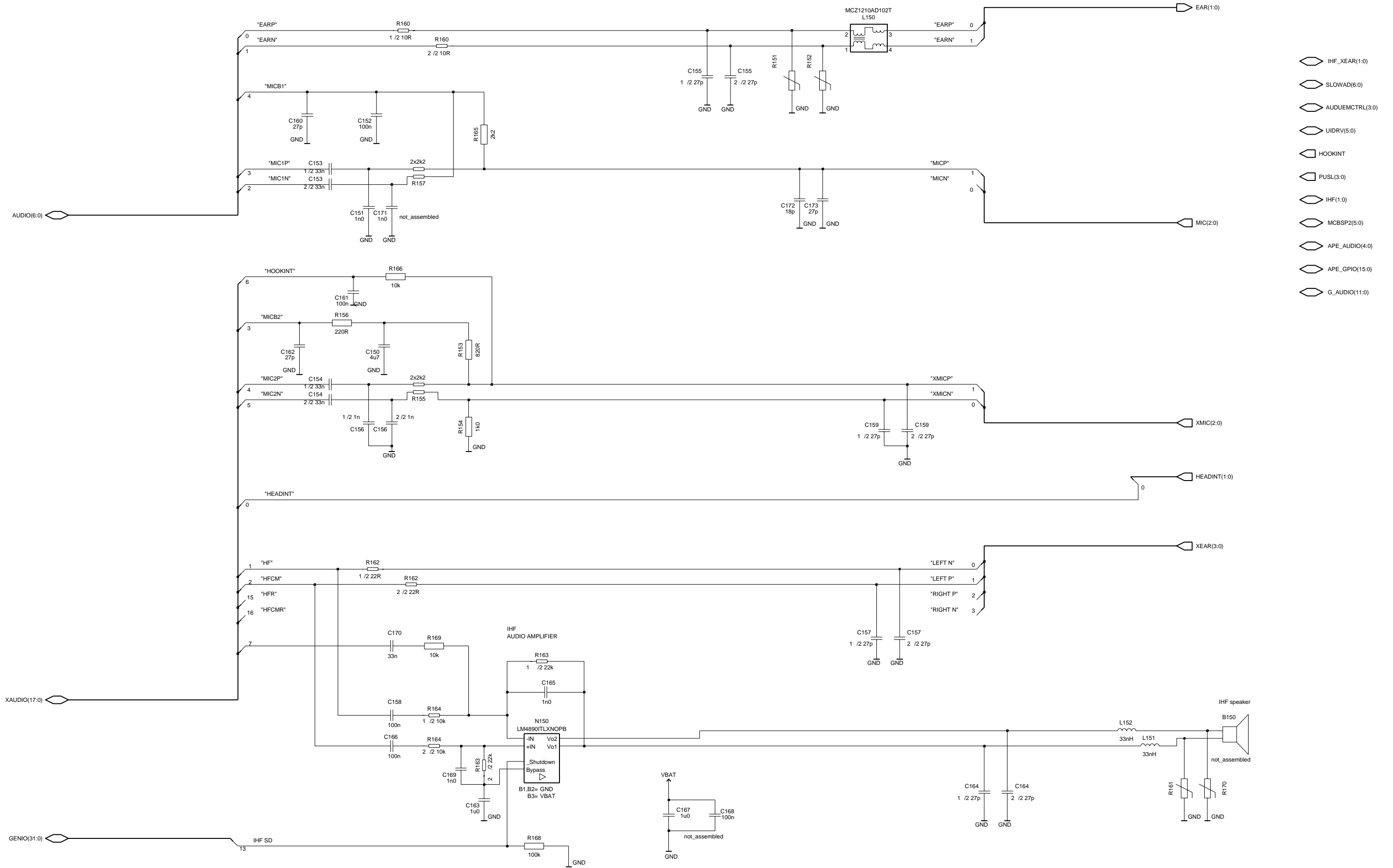
IPA1 AND IPA2 ARE USED IN RF, THE TOLERANCE OF R422 IS 1% (0402, 1430873)

System Connector v. 0.0 ed. 60



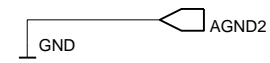
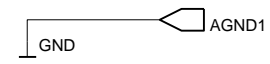
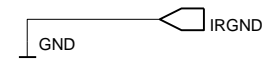
- ▷ PWRONX
- ▷ HOOKINT
- ▷ RFAUX(1:0)
- ▷ IHF(1:0)
- ▷ IACCDIF(5:0)
- ▷ SIMIF(3:0)
- ▷ AUDUEMCTRL(3:0)
- ▷ PUSL(3:0)
- ▷ USB_DIG(6:0)
- ▷ GPIO(31:0)
- ▷ FMANT
- ▷ USB(2:0)
- ▷ APE_GPIO(15:0)
- ▷ G_SYS_CONN(11:0)

Audio v. 0.0 ed. 55

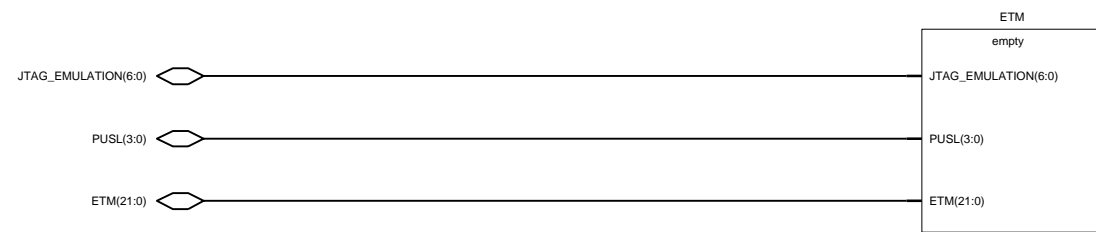


- IHF_XEAR(1:0)
- SLOWAD(6:0)
- AUDUEMCTRL(3:0)
- UIDRV(5:0)
- HOOKINT
- PUSL(3:0)
- IHF(1:0)
- MCBSP2(5:0)
- APE_AUDIO(4:0)
- APE_GPIO(15:0)
- G_AUDIO(11:0)

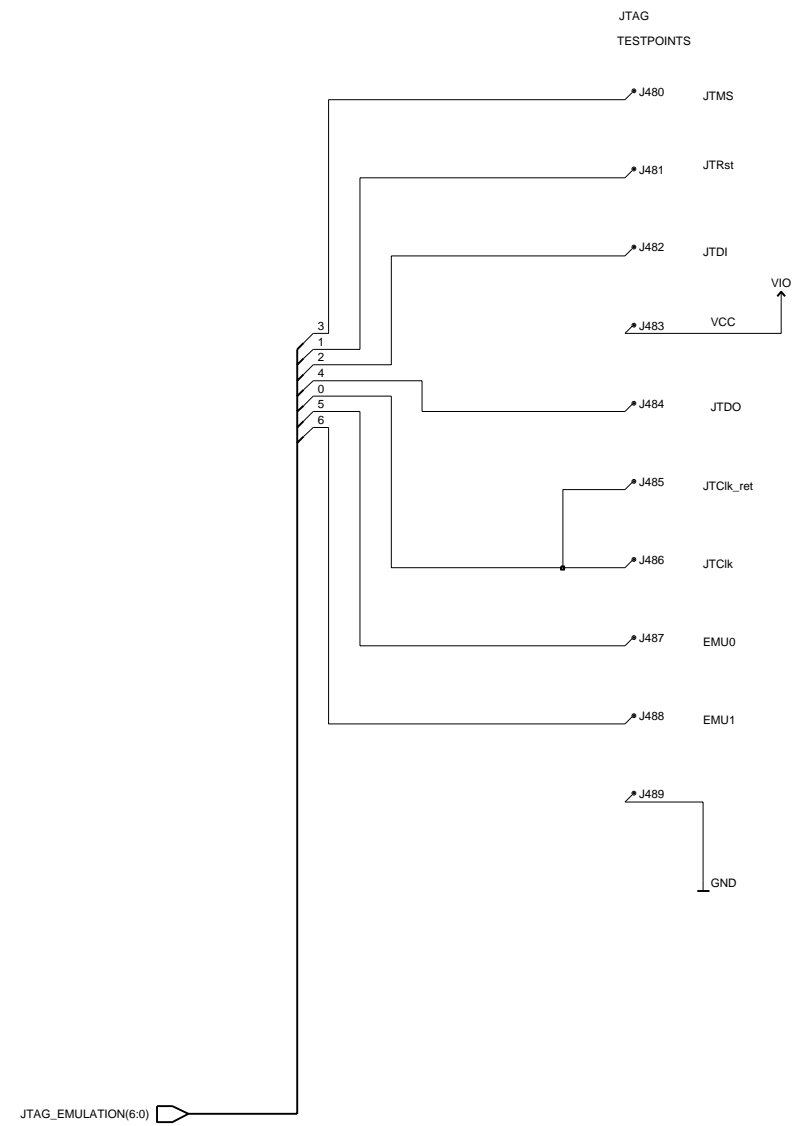
MultiGND Symbol Bypass v. 0.0 ed. 11



Test and Emulator Interface v. 2.0 ed. 42



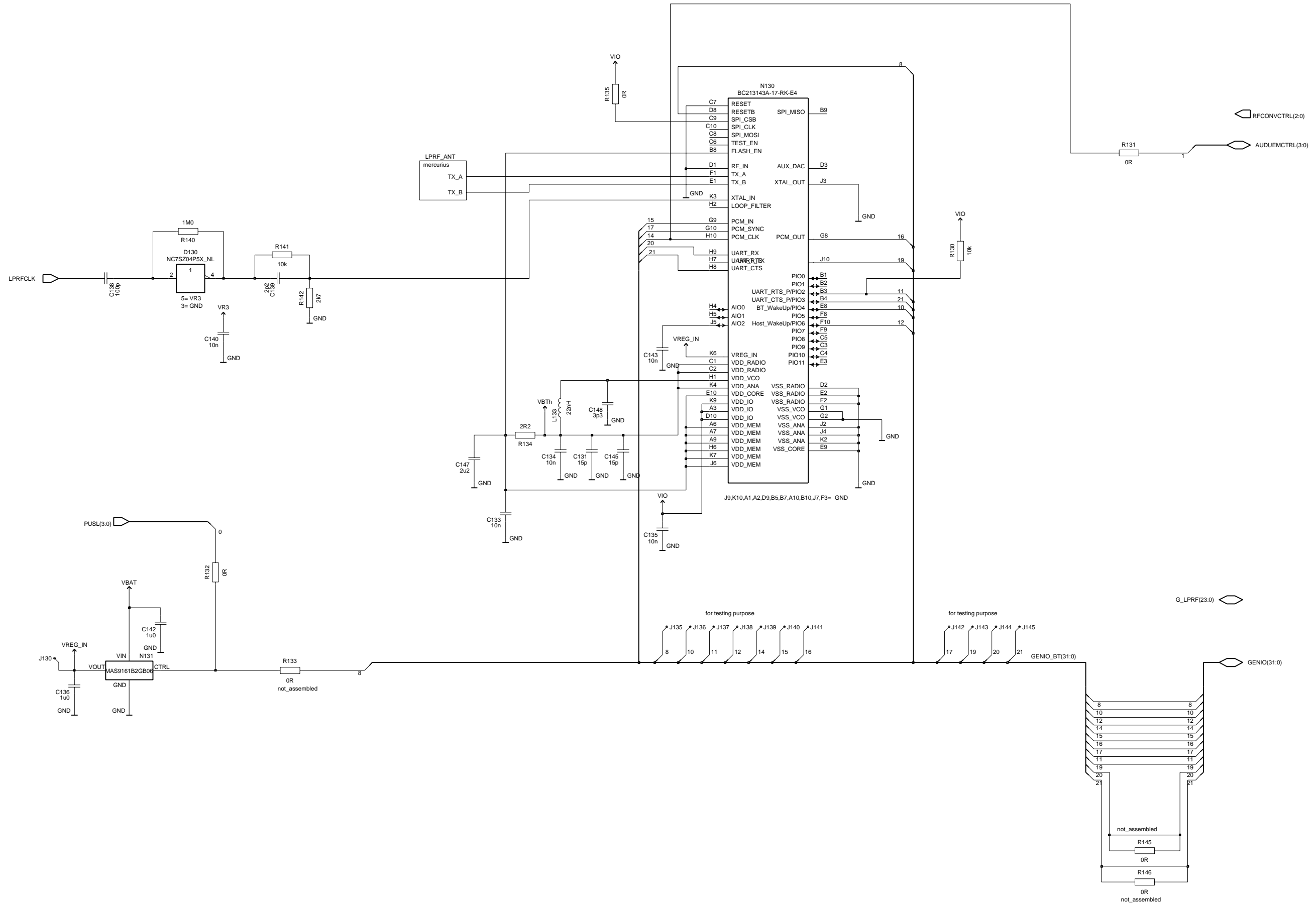
Testpoints for JTAG Emulator v. 1.3 ed. 12



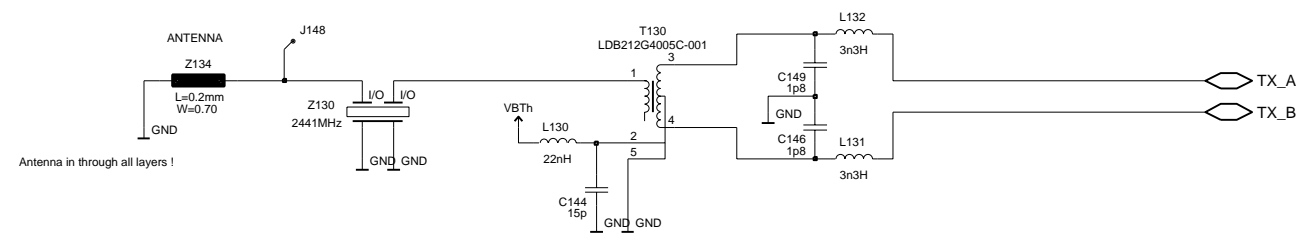
No Test Interface v. 1.3 ed. 7

DSP_MCU TEST(2.0) 

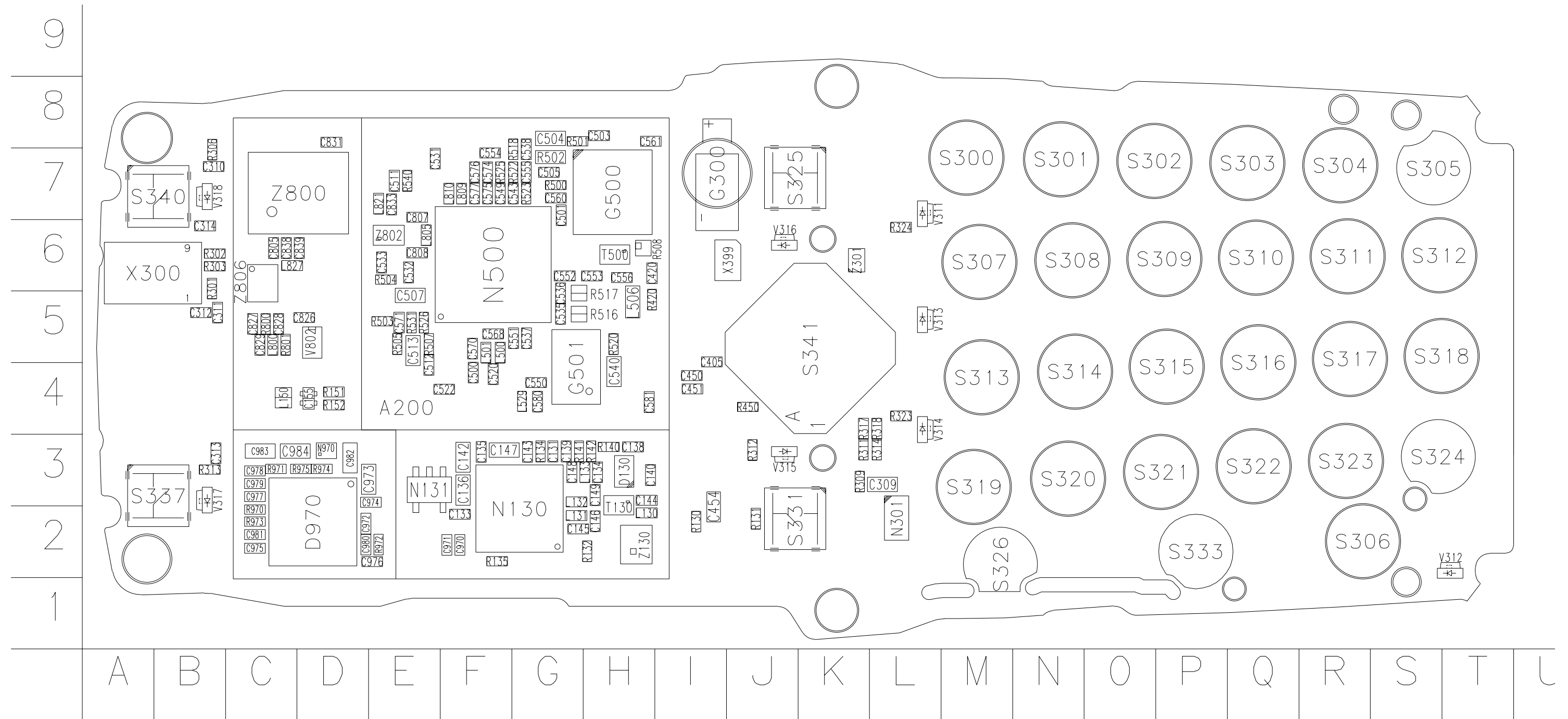
Module BlueCore2-ROM v.0 ed. 79



LPRF Antenna v. 0 ed. 21



Parts Placement Diagram TB6_21a top



Parts Placement Diagram TB6_21a bottom

